



NEW GENERATION FLEXIBLE HYBRID ELECTRONICS Cost-effective Assembly & Packaging Technologies

AGENDA

Registration opens – Continental Breakfast will be Served
Welcome and Introduction
KEYNOTE: Flexible Hybrid Electronics – Disrupting Conventional IC Packaging and System Design Solutions Wilfried Bair, Senior Engineering Manager, Device Integration and Packaging, NextFlex
The Rationale and Requirements for Pervasive Computing Jayna Sheats, CEO, Terecircuit
Advanced Low Dielectric Constant Materials: Learning and Perspectives Dr. Geraud DuBois, Head of Data Driven Science to Solutions Organization, Principal Research Staff Member, IBM Research / Adjunct Professor, Stanford University
Break
Process Design Kit (PDK) for Flexible Hybrid Electronics Dr. Tsung-Ching "Jim" Huang, Senior Research Scientist, Hewlett-Packard Labs (HPE Labs)
PDK Meets Materials & Process Database Jason Marsh, Directory of Technology, NextFlex
Lunch
KEYNOTE: An Alternative History of the Electronics Manufacturing Industry Joseph (Joe) Fjelstad, Founder & President, Verdant Electronics
Emerging Challenges of Power/Reliability Analysis for FHE Dr. Norman Chang, Chief Technologist, Semiconductor Business Unit, ANSYS, Inc., Co-Founder of Apache Design
Improving Health Through Continuous Blood Pressure Monitoring Dr. Xina Quan, Co-Founder and CTO, PyrAmes (a Stanford University spin-out)
Break
The Hidden Cost of Ground in Hybrid Electronic Interconnects Heidi Barnes, Senior Applications Engineer, Keysight Technologies
Materials Development for Flexible Systems and Next Generation Electronics Applications Chris Milasincic, New Business Development and Corporate Plans, DowDuPont Electronic Materials Division
Wrap up
NextFlex Manufacturing Tour

Schedule Subject to Change

There will be brief Sponsor Introductions after each presentation.

ANSYS, Inc.

NAMICS Corporation

- Promex Industries
- ConnecTEC Japan/America
 SMART Microsystems Ltd.
- Gel-Pak
- SoftMEMS LLC
 - Universal Instruments Corporation